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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/258,442	02/26/1999	SYUJI TAKADA	FUJI-15.894	3881

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[REDACTED] EXAMINER

NGUYEN, PHUONGCHAU BA

ART UNIT	PAPER NUMBER
2665	4

DATE MAILED: 09/24/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	09/258,442	TAKADA ET AL. <i>[Signature]</i>	
	<b>Examiner</b>	<b>Art Unit</b>	
	Phuongchau Ba Nguyen	2665	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE \_\_\_\_ MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 2-26-99 application.
- 2a) This action is FINAL.                    2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) Claim(s) 1-11 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_ is/are allowed.
- 6) Claim(s) 1-11 is/are rejected.
- 7) Claim(s) \_\_\_\_ is/are objected to.
- 8) Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 26 February 1999 is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on \_\_\_\_ is: a) approved b) disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

#### Priority under 35 U.S.C. §§ 119 and 120

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) All b) Some \* c) None of:
    1. Certified copies of the priority documents have been received.
    2. Certified copies of the priority documents have been received in Application No. \_\_\_\_ .
    3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
  - a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

#### Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). ____ .
2) <input checked="" type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) 3 .	6) <input type="checkbox"/> Other: _____

*Drawings*

1. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g).
2. The drawings are objected to because figure 2 should be labeled with descriptive legends, i.e., in figure 2, number 20 should be labeled as 20 (Buffer Memory Control). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application.

The objection to the drawings will not be held in abeyance.

/ *BL*

*Claim Rejections – 35 USC § 102*

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

4. Claims 1, 11, 2-6 are rejected under 35 U.S.C. 102(e) as being anticipated by Sakurai (6,016,317).

Regarding claims 1, 11, 2-6:

-As claim 1, Sakurai (6,016,317) discloses a common buffer memory control apparatus (10) controlling a common buffer memory (11) which is used to store message data items each of which is divided into a plurality of cells based on an asynchronous transfer mode, said apparatus comprising:

first management means (col.5, line 61 to col.6, line 7; 103 in fig.2) for managing whether each of blocks into which said common buffer memory divided is free or used;

block selecting means (next address; col.5, line 58 to col.6, line 7) for selecting a block of said common buffer memory which is free based on information obtained by said first management means; and cell writing control means (col.5, lines 52–61) for controlling a write operation for a single message data item so that the respective cells of the single message data item are written in the block, selected by said block selecting means (i.e., next address), of said common buffer memory.

-As claim 11, The common buffer memory control apparatus as claimed in claim 1, wherein said block selecting means selects a block of said common buffer memory which is free in advance of receiving a head cell positioned at a head of the message data {Sakurai, col.6, lines 40–50}.

-As claim 2, The common buffer memory control apparatus as claimed in claim 1, wherein said first management means comprises:

a free block management table (103, fig.2) having areas each of which corresponds to one of the blocks of said common buffer memory, each of the

areas of said free block management table storing information indicating whether a corresponding one of the blocks of said common buffer memory is free or used, wherein said block selecting means selects, with reference to said free block management table, the block which is free {also, col.6, lines 43-50, Sakurai}.

-As claim 3, The common buffer memory control apparatus as claimed in claim 2, wherein said block selecting means comprises:

free block searching means for searching said free block management table for an area storing the information indicating that a corresponding block is free, wherein the block corresponding to the area obtained by said free block searching means is selected {next address; col.5, line 58 to col.6, line 7, Sakurai}.

-As claim 4, The common buffer memory control apparatus as claimed in claim 1, wherein said cell writing control means comprises:

second management means {col.5, lines 58–61, Sakurai} for managing addresses in each of the blocks of said common buffer memory; and address specifying means {col.5, line 65 to col.6, line 1, Sakurai} for specifying, based on information obtained by said second management means, an address in the block selected by said block selecting means every time one of cells of the single message data item is received, so that the cells of the single message data item are written at addresses specified by said address specifying means.

-As claim 5, The common buffer memory control apparatus as claimed in claim 4, wherein second management means comprises:

a table (101, fig.2) having areas each of which corresponds to one of the blocks of said common buffer memory, each of the areas storing an address at which the next cell should be written, the address in each of the areas of said table being updated every time a cell is written in a corresponding block of said common buffer memory {also, col.6, lines 18–25, 43–49, Sakurai}.

-As claim 6, The common buffer memory control apparatus as claimed in claim 1, wherein a number of blocks of said common buffer memory and a length of each of the blocks are set at values corresponding to data supplied from an external input device (input lines L14-L16) {col.5, lines 52-58, Sakurai}.

*Claim Rejections – 35 USC § 103*

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 7-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sakurai in view of Wicklund (6,185,209).

**Regarding claims 7-8:**

-As claim 7, Sakurai does not disclose the claimed feature. However, in the same field of endeavor, Wicklund (6,185,209) discloses in ATM switching device for setting head-in-pointer and tail-in-pointer for the cell just written {fig.3,

steps 3–7, 3–9} thus determining the length of each of the blocks of said common buffer memory is controlled based on information about an amount of information of the message data extracted from a cell. Therefore, it would have been obvious to an artisan to apply Wicklund's teaching into Sakurai's teaching and the motivation being to determine length of merged or pre-merged VC queues by using the control words {col.4, lines 22–24}.

–As claim 8, Wicklund further discloses that if the amount of information of the message data actually written in the block is less than the controlled length of the block (i.e., Head-in, wherein it is not yet the last of a packet), a remaining area of the block is opened so as to be used in following message communication {col.5, lines 30–54}.

7. Claims 9–10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sakurai in view of Cotton (5,535,197)

**Regarding claims 9–10:**

-As claim 9, Sakurai does not explicitly disclose the claimed feature. However, in the same field of endeavor, Cotton (5,535,197) discloses in figure 6 wherein the common buffer memory control apparatus (cell manager 13) further comprising user management means (buffer memory allocation-linked list) for managing the blocks of said common buffer memory so that a block storing a head cell positioned at a head (640) of the message data corresponds to user identification information (the assigned buffer address which the head pointed the cell into) extracted from the head cell, wherein said cell writing control means controls, based on information obtained by said user management means, the write operation so that each of cells positioned in a mid-portion (not at the head of linked list) of the message data is written in the block corresponding to user identification information (assigned buffer address) extracted from said each of the cells {col.13, lines 4-45}. Therefore, it would have been obvious to an artisan to apply Cotton's teaching into Sakurai's teaching and the motivation being to maintain a current list of the unassigned buffer address available for assignment to incoming cells {col.12, lines 65-67, Cotton}.

-As claim 10, The common buffer memory control apparatus as claimed in claim 9, wherein said user management means comprises:

a user management table (fig.6, Cotton) having areas each of which corresponds to one of the blocks of said common buffer, each of the areas storing the user identification information extracted from the head cell which has been written in a corresponding block of said common buffer memory, wherein said cell writing control means controls the write operation with reference to said user management table.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phuongchau Ba Nguyen whose telephone number is 703-305-0093. The examiner can normally be reached on Monday-Friday from 10:00 a.m. to 3:00 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Huy Vu can be reached on 703-308-6602. The fax phone numbers for the organization where this application or proceeding is

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assigned are 703-872-9314 for regular communications and 703-872-9314  
for After Final communications.

Any inquiry of a general nature or relating to the status of this application  
or proceeding should be directed to the receptionist whose telephone number  
is 703-305-4700.

PN

Phuongchau Ba Nguyen  
Examiner  
Art Unit 2665

September 19, 2002

  
9/19/02